

EA51 - [09881472.wsp.1]

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Failed

Saved

Favorites

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Queue

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L1: (47) (((((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3 circuit)) and ((address wordline) adj3 decoder)) and (isolation or ISO) same (second with (array bank block))) and (timing adj3 circuit)) and ((tim\$3 adj3 circuit) with input) same (sense adj2 (amp amplifier))) same decoder same output same (array block bank) (timing adj2 circuit) with (input and output) ((timing adj2 circuit) with input) with ((timing adj2 circuit) with output) L15 with (ISO (isolat\$3 adj2 signal)) L15 with (ISO (isolation adj2 signal))

USPAT:USPGPUB:EPQ:IPC:DERWENT:IBM:TDB

Default operator: DR

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Search Text

	Type	L #	Hits	Search Text	DBs	TLI
1	BRS	L1	47	(((sense adj2 (amp amplifier)) with (shar\$3 switch\$3 select\$3 circuit)) and ((address wordline) adj3 decoder)) and (isolation or ISO) same (second with (array bank block))) and (timing adj3 circuit)) and ((tim\$3 adj3 circuit) with input) same (sense adj2 (amp amplifier))) same decoder same output same (array block bank)	USPAT	200
2	BRS	L10	2	L9 and ((ISO isolation) adj3 signal)	USPAT	200
3	BRS	L9	102	((tim\$3 adj3 circuit) with input) same (sense adj2 (amp amplifier))) same decoder same output same (array block bank)	USPAT	200
4	BRS	L14	7144	(timing adj2 circuit) with (input and output)	USPAT	200
5	BRS	L15	5793	((timing adj2 circuit) with input) with ((timing adj2 circuit) with output)	USPAT	200
6	BRS	L17	4	L15 with (ISO (isolat\$3 adj2 signal))	USPAT	200
7	BRS	L16	4	L15 with (ISO (isolation adj2 signal))	USPAT	200

Ready

1/1

